

2.1

Analog to digital/Digital to analog

Digital signals:

- Originate from analog signals
- Are generated by digital generators

Theoretical description of an A/D converter

$$X_a(\omega) = \int_{-\infty}^{\infty} x_a(t) e^{-j\omega t} dt \qquad X(\theta) = \sum_{n=-\infty}^{\infty} x(n) e^{-jn\theta}$$

$$x_a(t) = \frac{1}{2\pi} \int_{-\infty}^{\infty} X_a(\omega) e^{j\omega t} d\omega \qquad x(n) = \frac{1}{2\pi} \int_{-\pi}^{\pi} X(\theta) e^{jn\theta} d\theta$$

Time-domain description of the ideal A/D converter:

$$x(n) = x_a(nT)$$

Frequency domain description? \rightarrow analog

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$$x_a(nT) = \frac{1}{2\pi} \int_{-\infty}^{\infty} X_a(\omega) e^{j\omega nT} d\omega \qquad \omega T = \theta$$

$$= \frac{1}{2\pi} \int_{-\infty}^{\infty} X_a\left(\frac{\theta}{T}\right) e^{jn\theta} \frac{d\theta}{T} = \frac{1}{2\pi} \sum_{k=-\infty}^{\infty} \frac{1}{T} \int_{k2\pi-\pi}^{k2\pi+\pi} X_a\left(\frac{\theta}{T}\right) e^{jn\theta} d\theta \qquad \theta \rightarrow \theta + 2k\pi$$

$$= \frac{1}{2\pi} \sum_{k=-\infty}^{\infty} \frac{1}{T} \int_{-\pi}^{\pi} X_a\left(\frac{\theta + 2k\pi}{T}\right) e^{jn(\theta + 2k\pi)} d(\theta + 2k\pi)$$

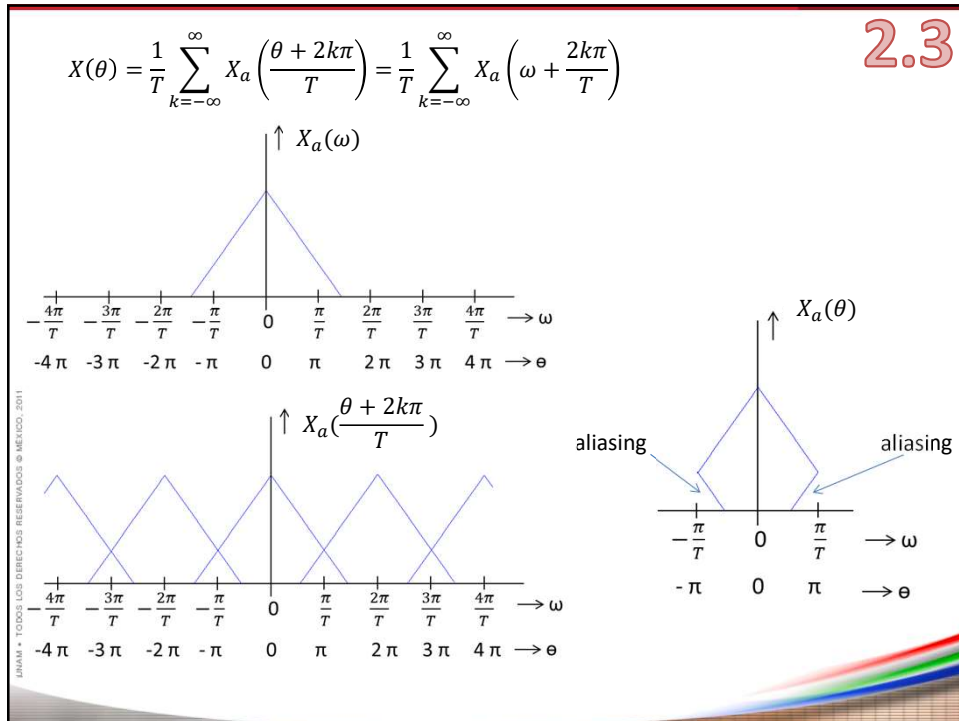
$$= \frac{1}{2\pi} \sum_{k=-\infty}^{\infty} \frac{1}{T} \int_{-\pi}^{\pi} X_a\left(\frac{\theta + 2k\pi}{T}\right) e^{jn\theta} d\theta$$

$$= \frac{1}{2\pi} \int_{-\pi}^{\pi} \left\{ \frac{1}{T} \sum_{k=-\infty}^{\infty} X_a\left(\frac{\theta + 2k\pi}{T}\right) \right\} e^{jn\theta} d\theta$$

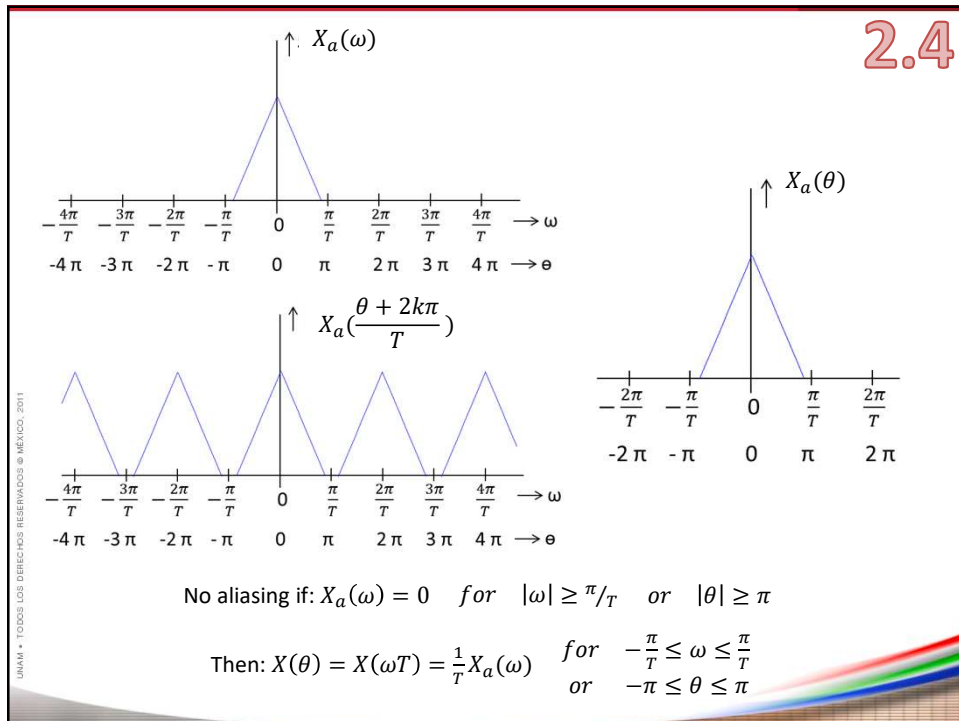
$$x(n) = \frac{1}{2\pi} \int_{-\pi}^{\pi} X(\theta) e^{jn\theta} d\theta \qquad \left. \vphantom{x(n)} \right\} X(\theta) = \frac{1}{T} \sum_{k=-\infty}^{\infty} X_a\left(\frac{\theta + 2k\pi}{T}\right)$$

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Practical A/D convertor

Block diagram showing the process: $x_a(t) \rightarrow \text{Sample/Hold} \rightarrow y_a(t) \rightarrow \text{Set of comparators} \rightarrow \text{Binary number}$.

Another block diagram shows: $x_a(t) \rightarrow \text{Sample Hold} \rightarrow y_a(t) \rightarrow \text{Delay} \rightarrow y_a(t-T) \rightarrow \text{A/D} \rightarrow x(n) \rightarrow \text{Q} \rightarrow x_d(n)$.

Graph showing the staircase approximation \hat{x} of the signal $x(n)$. The step height is q . The maximum error is $\hat{x} = 2^{b-1}q$. The error range is $-\frac{q}{2} \leq x_d(n) - x(n) \leq \frac{q}{2}$.

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Influence of Q

Graphs illustrating the influence of quantization: $x(n)$, $x_d(n)$, and $e(n)$.

Block diagram showing the addition of quantization error: $x(n) \rightarrow \text{+} \rightarrow x_d(n) = x(n) + e(n)$.

Power σ_x^2 of a digital signal:

$$\sigma_x^2 = \frac{1}{q} \int_{-q/2}^{q/2} (x - \mu)^2 dx \quad \text{but} \quad \mu = 0$$

$$\sigma_x^2 = \int_{-q/2}^{q/2} \frac{1}{q} x^2 dx = \frac{1}{q} \left. \frac{x^3}{3} \right|_{-q/2}^{q/2} = \frac{1}{3q} \left[\frac{q^3}{8} + \frac{q^3}{8} \right] = \frac{q^2}{12}$$

Noise power: $\frac{q^2}{12}$

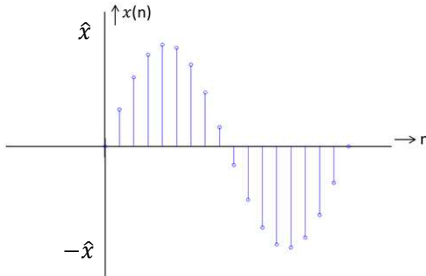
Quantization error $e_1(n) = \begin{cases} qn/2N & |n| \leq N \\ 0 & |n| > N \end{cases}$

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Maximum signal power for a sine wave



B bits

$$\hat{x} = 2^{B-1}q$$

$$x_{EFF} = \frac{1}{\sqrt{2}} 2^{B-1}q$$

$$P_x = \frac{1}{2} 2^{2B-2} q^2 = \frac{2^{2B} q^2}{8}$$

Signal to noise ratio: $\frac{S}{N} = \frac{P_x}{P_n} = \frac{\frac{2^{2B} q^2}{8}}{\frac{q^2}{12}} = \frac{12}{8} 2^{2B} = \frac{3}{2} 2^{2B}$

In decibels: $\frac{S}{N} = 10 \log \left(\frac{3}{2} 2^{2B} \right) = 10 \log(2^{2B}) + 10 \log \left(\frac{3}{2} \right)$

$$= 20B \log(2) + 1.75 = 6B + 1.75$$

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Proof that: $\sum_{n=1}^N n^2 = \frac{N(N+1)(2N+1)}{6}$

$N = 1$: $\sum_{n=0}^1 n^2 = 0^2 + 1^2 = 1 = \frac{1 \cdot 2 \cdot 3}{6}$ True

Suppose $\sum_{n=1}^N n^2 = \frac{N(N+1)(2N+1)}{6}$ is true for N_1

Prove that $\sum_{n=1}^{N+1} n^2 = \frac{(N+1)(N+2)(2N+3)}{6}$

$$\begin{aligned} \sum_{n=1}^{N+1} n^2 &= (N+1)^2 + \sum_{n=1}^N n^2 = (N+1)^2 + \frac{N(N+1)(2N+1)}{6} \\ &= \frac{6(N+1)^2 + N(N+1)(2N+1)}{6} = \frac{(N+1)(6N+6+2N^2+N)}{6} \\ &= \frac{(N+1)(2N^2+7N+6)}{6} = \frac{(N+1)(N+2)(2N+3)}{6} \end{aligned}$$

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ADC Converters 2.8b

Flash converter

Multistage converter

One bit converter

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Theoretical description of a D/A converter 2.9

$$y_a(t) = \sum_{n=-\infty}^{\infty} y(n)\delta_a(t - nT)$$

$$Y_a(\omega) = \int_{-\infty}^{\infty} y_a(t)e^{-j\omega t} dt = \int_{-\infty}^{\infty} \sum_{n=-\infty}^{\infty} y(n)\delta_a(t - nT) e^{-j\omega t} dt$$

$$= \sum_{n=-\infty}^{\infty} y(n) \int_{-\infty}^{\infty} \delta_a(t - nT) e^{-j\omega t} dt = \sum_{n=-\infty}^{\infty} y(n) e^{-jn\omega T} = \sum_{n=-\infty}^{\infty} y(n) e^{-jn\theta} = Y(\theta)$$

Therefore: $Y_a(\omega) = Y(\theta) = Y(\omega T)$

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Ideal A/D and D/A process 2.10

Reconstruction of $X_a(\omega)$?

Reconstruction filter

$$h_a(t) = \frac{\sin(\pi t/T)}{\pi t/T}$$

See signal analysis page 5.4 and 5.5

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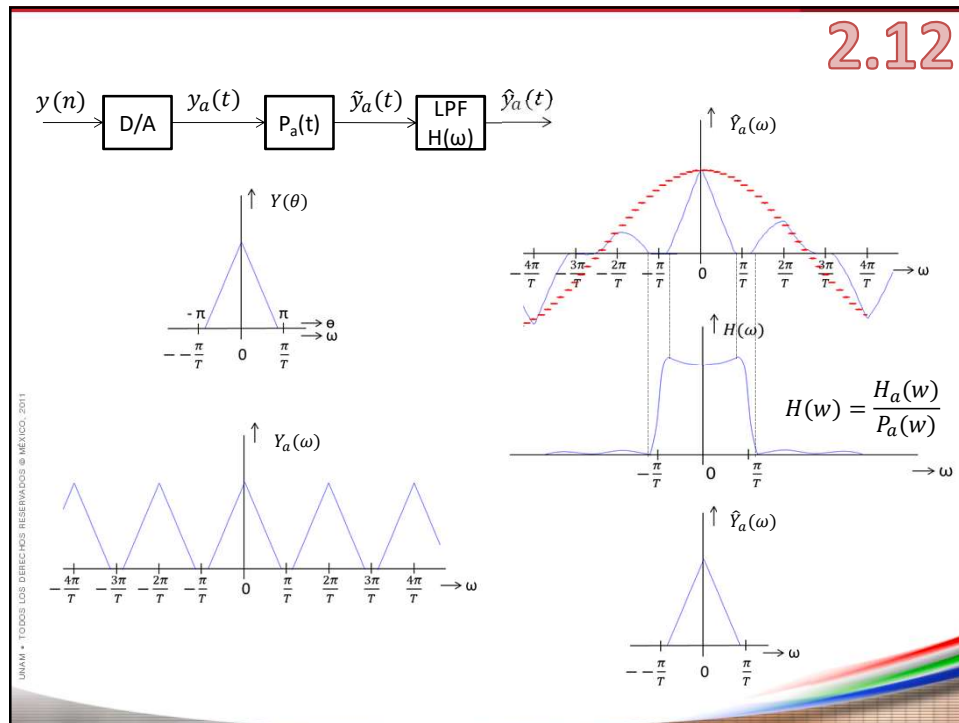
Practical D/A convertor 2.11

$$\tilde{y}_a(t) = \sum_{n=-\infty}^{\infty} y(n)P_a(t - nT)$$

$$\tilde{Y}_a(\omega) = Y_a(\omega)P_a(\omega)$$

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